PARAMETER POPULATION OF CELLS OF A HIERARCHICAL SEMICONDUCTOR STRUCTURE VIA FILE RELATION

to appropriate global geometric [values] variables from the global file such that changes of global geometric variables in the global file may cause changes in the design cells in accordance with parameters in the local files.

- A computer-readable medium having a computer program stored thereon 9. (Amended) to cause a suitably equipped computer to update a set of geometric parameters of a design cell by relating local geometric variables of a local file for the design cell to a global file of global geometric variables relating to layout of element blocks of a hierarchical semiconductor structure such that changes of a global geometric variable in the global file may cause changes in the cells in accordance with parameters in the local files.
- A computer comprising: 15. (Amended)
  - a processor;
  - a computer-readable medium;
- a global file of global geometric variables stored on the medium, at least some of the global geometric variables relating to physical layout of element blocks of a hierarchical semiconductor structure;
- a plurality of local files stored on the medium, each local file containing parameters relating a plurality of local variables to the global geometric variables; and,
- a computer program executed by the processor from the medium to automatically update a set of parameters for each of a plurality of programmable design cells, each cell having a corresponding local file, by reading, from the global file, values for the global geometric variables to which the local variables of the local file correspond.
- A computerized method comprising: 22. (Amended)
- changing within a cleansheet file at least one of a plurality of design rules for defining the physical layout of a hierarchical semiconductor structure;
- updating values for a plurality of global geometric variables of a global file based on the design rules of the cleansheet file; and,